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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) PTO Form 1449 Page 1 of 3	Attorney Docket No.: 046914-5001-01	Application No.: Unassigned
	Applicants: Robert Penn CAGLE, et al.	
	Filing Date: June 6, 2001	Group Art Unit: Unassigned 2153

6/24/01
 09/07/01
 06/06/01

U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date
K	5,737,514	April 7, 1998	Stiffler	395	182.11	Nov. 29, 1995
	4,412,281	Oct. 25, 1983	Works	364	200	July 11, 1980
	5,805,785	Sept. 8, 1998	Dias, et al.	395	182.02	Feb. 27, 1996
	4,967,344	Oct. 30, 1990	Scavezze, et al.	364	200	March 1, 1988
	4,710,926	Dec. 1, 1987	Brown, et al.	371	9	Dec. 27, 1985
	5,822,512	Oct. 13, 1998	Goodrum, et al.	395	182.11	June 5, 1996
	5,345,566	Sept. 6, 1994	Tanji, et al.	395	325	Jan. 24, 1992
	5,404,465	April 4, 1995	Novakovich, et al.	395	325	March 10, 1993
	5,784,547	July 21, 1998	Dittmar, et al.	395	182.02	March 18, 1996
	5,852,724	Dec. 22, 1998	Glenn, II et al.	395	200.69	June 18, 1996
	6,052,797	April 18, 2000	Ofek, et al.	714	6	Aug. 20, 1998
	6,035,412	March 7, 2000	Tamer, et al.	714	6	March 19, 1997
	6,092,214	July 18, 2000	Quoc, et al.	714	4	Nov. 6, 1997
	6,092,218	July 18, 2000	Liddell, et al.	714	11	March 22, 1999
	5,968,185	Oct. 19, 1999	Bressoud, et al.	714	10	July 16, 1998
V	5,513,314	April 30, 1996	Kandasamy, et al.	395	182.04	Jan. 27, 1995

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Sub Class	Translation YES NO	

Examiner: KRISNA LIM	Date Considered: 9/30/04
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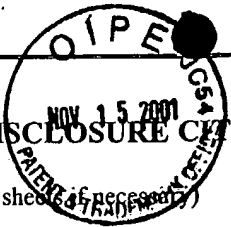
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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) PTO Form 1449 Page 2 of 3		Attorney Docket No.: 046914-5001-01	Application No.: Unassigned 09/874,249
		Applicants: Robert Penn CAGLE, et al.	
		Filing Date: June 6, 2001	Group Art Unit: Unassigned 2153
		OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
K		W.Baker, R. Horst, D.Sonnier and W. Watson; "A Flexible ServerNet-based Fault-Tolerant Architecture"; 1995 IEEE	
		Delphion Intellectual Property Network website; "Bus Architecture for Passive Fault-Tolerant Command/Response System"; August 1986	
		Delphion Intellectual Property Network website; "Method for Improving Network Availability with Redundant Network Servers"; August 1996	
		Y. Liu, F. Yang and J. Chen; "A Two-Level Strategy for Software Fault Tolerance of Service Control Point"; 1998 IEEE	
		M. Gouda and T. McGuire; "Accelerated Heartbeat Protocols"; 1998 IEEE	
		T.Nakamikawa, Y. Morita, S. Yamaguchi, S. Ishikawa and Y. Miyazak; "High Performance Fault Tolerant Computer and its Fault Recovery"; 1997 IEEE	
		H.Kuefner and H. Baehring; "Dynamic Fault Tolerance in DCMA - A Dynamically Configurable Multi computer Architecture"; 1996 IEEE	
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		W. David Shambroom; "Use of Protocol Validation and Verification Techniques in the Design of a Fault-Tolerant Computer Architecture"; 1993 IEEE	
		L. Strigini and F. Di Giandomenico; "Flexible Schemes for Application-Level Fault Tolerance"; 1991 IEEE	
V		B. Butler and S. Adams; "A Fault-Tolerant Network Architecture for Integrated Avionics"; 1991 IEEE	
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		Filing Date: June 6, 2001	Group Art Unit: Unassigned 2153
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
✓		Jean-Claude Laprie, Jean Arlat, Christian Beounes and Karama Kanoun; "Definition and Analysis of Hardware-and Software-Fault-Tolerant Architectures"; July 1990 IEEE	
✓		F.Cristian; B. Dancey and J. Dehn; "Fault-Tolerance in the Advanced Automation System"; 1990 IEEE	
✓		S. Bagchi, B. Srinivasan, K. Whisnant, Z. Kalbarczyk and R. Iyer; "Hierarchical Error Detection in a Software Implemented Fault Tolerance (SIFT) Environment", 2000 IEEE	
✓		Flavin Cristian; "Understanding Fault-Tolerant Distributed System"; Communications of the Ages, February 1991/Vol. 34/No. 2	
✓		Daniel P. Siewiorek/Carnegie Mellon University; "Fault Tolerance in Commercial Computer"; 1990 IEEE	
✓		Philip A. Bernstein; "Sequoia: A Fault-Tolerant Tightly Coupled MultiProcessor for Transaction Processing"; February 1988 IEEE	
✓			
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Attorney Docket No.:
46914-5001-01

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Filing Date: June 6, 2001

Group: 2154

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U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Sub Class	Filing Date

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Sub Class	Translation YES NO

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Wang Qian et al, "FAULT - TOLERANT COMPUTER SYSTEM FOR STD", Mini-Micro Systems, Vol. 16, No. 11, Nov. 1995.

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FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Sub Class	Translation YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>K</i>	David Guest, "The foolproof computer", Computer Systems Europe December 1988.
<i>V</i>	V. A. BOGATYREV, "Estimating the Reliability of Functionally Redundant Multicomputer Systems with Their Reconfiguration on the Basis of Function Reallocation", Engineering Simulation, 1995, Vol. 12, pp. 318-322.
<i>S</i>	Se Hwa Park et al., "A Study on the Implementation of a Control System with Dual Structure and Its Reliability Analysis", Dept. of Electrical Eng., KAIST, 1990.
<i>T</i>	Tetsuo SAKUMA et al., "OKITRON-II: Fault-Tolerant Telecom Computer, OKI Technical Review 156 Vol. 62, July 1996.
<i>V</i>	V. A. BOGATYREV, "On The Dynamic Distribution of Intercomputer Communication Requests in Multicomputer Systems with Redundant Buses", Engineering Simulation, 11(2), 1993, pp. 234-240.
<i>J</i>	J. C. CHENAVAS et al., "Architecture Des Systems De Conduite A Haute Disponibilite".
<i>A</i>	Aguilera et al., "Heartbeat: A Timeout-Free Failure Detector for Quiescent Reliable Communication", 1977.
<i>P</i>	Peter Ruber "How Much Server Fault", 1998.

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